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TALK : [*Algorithmic design of switched-capacitor DC-DC converters using the Fibonacci & Binary methods*](#)

Subhajit Sen passed with B.Tech in Electronics Engineering from Institute of Technology, Banaras Hindu University, Varanasi, India in 1984, M.S. from Louisiana State University, U.S.A. in 1991 and with Ph.D in Electrical & Computer Engineering from University of Waterloo, Ontario, Canada in 1997. Between 1984 to 1988 he worked at Semiconductor Complex Ltd., Chandigarh, India on analog CMOS circuit design. Subsequent to his Ph.D studies and till 2009 he has worked in the Indian semiconductor industry (Arcus Technologies, Cypress Semiconductors, Cirrus Logic, Sasken Communications, SiRF Technologies) both as a senior level individual contributor as well as project manager for the design of analog modules and sub-systems for fiber-optic communication, audio, video, defense-electronics and GPS related products. He has two U.S. patents in the areas of trans-impedance amplifier(TIA) and PLL charge-pump. He worked at Dhirubhai Ambani Institute of Information & Communication Technology(DA-IICT), Gandhinagar as Associate Professor from April 2009 – May 2013. His general interests are in analog and mixed-signal VLSI integrated circuit & system design, digitally-assisted analog circuit design, embedded-systems for biomedical and energy-harvesting applications. He is also interested in history of Indian science and technology.